

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): <b>VAIDYANATHAN, Rema</b>	Group Arit Unit: <b>2615</b>
Application No.: <b>09/976,600</b>	Examiner: <b>SWERDLOW, Daniel</b>
Filed: <b>October 12, 2001</b>	Notice of Allowance: <b>July 11, 2006</b>
Title: <b>SYSTEM AND METHOD FOR CONTROLLING TRANSMITTER OUTPUT LEVELS IN A WIRELESS COMMUNICATIONS</b>	

**TRANSMITTAL OF FORMAL DRAWINGS**

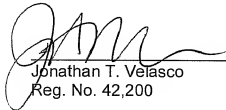
Commissioner for Patents  
Official Draftsperson  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

Enclosed, please find four (4) sheets of formal drawings for the above-identified application.

Respectfully Submitted;

Dated: Aug 31, 2006

  
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